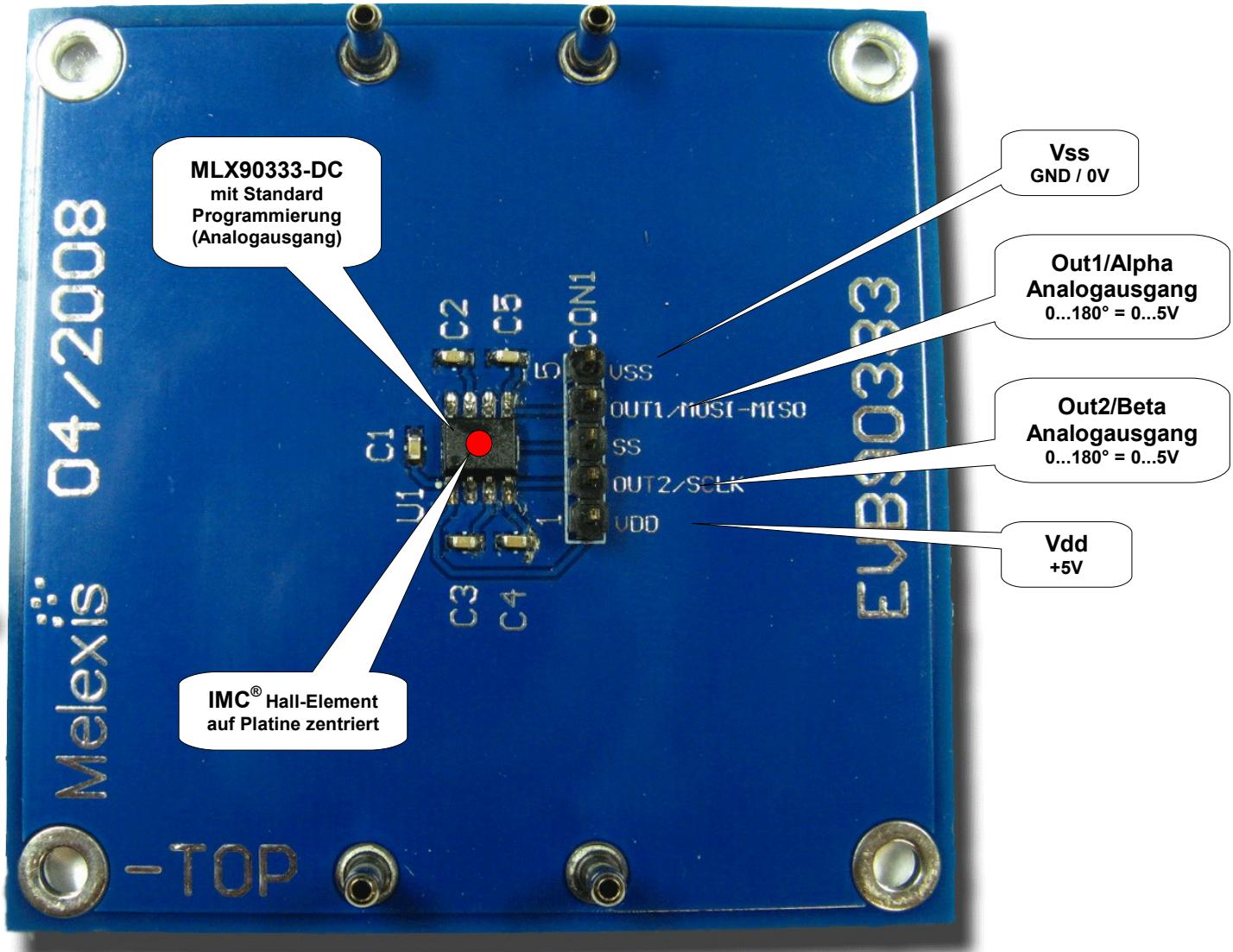
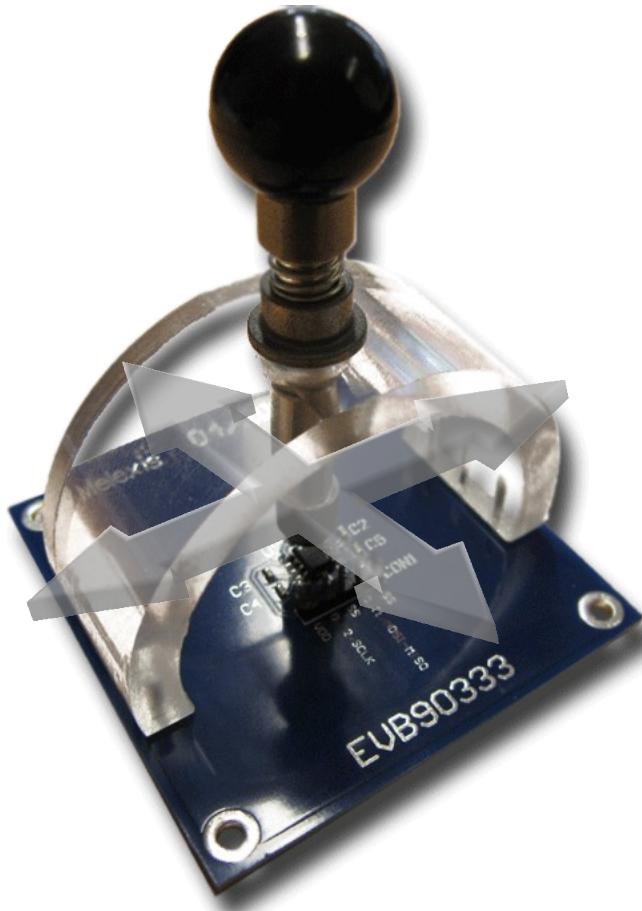


Melexis Evaluation Board

EVB90333-DC
(SOIC8)



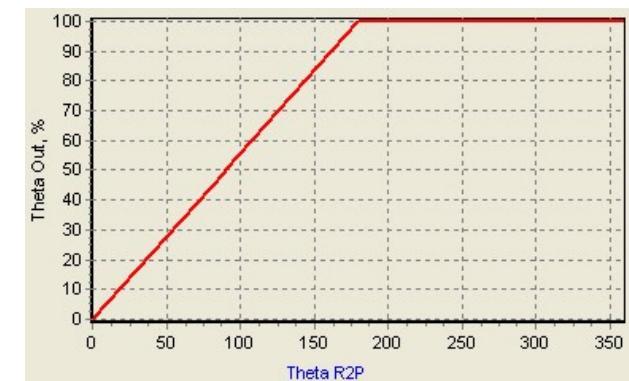
Melexis Evaluation Board

EVB90333-DC (SOIC8)

EEPROM-Konfiguration des MLX90333BCH auf dem EVB

MLXLOCK [8]	<input type="checkbox"/>	<input checked="" type="checkbox"/>
Oscillator frequencies		
High speed mode [6]	3F	3F
Low power mode [6]	10	10
T25 [8]	99	99
ITRIM [3]	02	02
DACTHRES [8]	F0	F0
PWMT [16]	1000	1000
XOFSSLOPE [16]	0509	0509
YOFSSLOPE [16]	FDF	FDF
Melexis ID		
ID1 [16]	0CC1	0CC1
ID2 [16]	9BEE	9BE
ID3 [16]	171F	171F
Sensitivity mismatch		
SMISM [8]	3B	3B
SxGTsTy [1]	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
Orthogonality error		
Radians [16]	0000	0000
Residual offset correction factors		
X0 [8]	06	06
Y0 [8]	ED	ED
Z0 [8]	08	08
LOCK [8]	<input type="checkbox"/>	<input type="checkbox"/>
Calibration and debug modes		
ResetOnFault [2]	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
DisableGainCtrl [1]	<input type="checkbox"/>	<input type="checkbox"/>
Output stages mode		
Out1Enable	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
Out1PWM [1]	<input type="checkbox"/>	<input type="checkbox"/>
Out1Mode [3]	02	02
Out2Enable	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
Out2PWM [1]	<input type="checkbox"/>	<input type="checkbox"/>
Out2Mode [3]	02	02
Filter [8]	03	03
Filter A1 [16]	6600	6600
Filter A2 [16]	2A00	2A00
Clamping		
CLAMPLOW [%]	0.0	0.0
CLAMPHIGH [%]	100.0	100.0
Custom ID		
ID1 [16]	0001	0001
ID2 [16]	0011	0011
ID3 [16]	006C	006C
Correction factors		
KZ [8]	1.398	1.39
KT [8]	1.000	1.00

Ausgangskennlinie „Alpha“ Analogausgang 0...180° = 0...5V



Ausgangskennlinie „Beta“ Analogausgang 0...180° = 0...5V

